# TEC-VERSE 2026

*Draft Session Plan*

**26-27 November 2026 | Chennai Trade Centre, Nandambakkam, Chennai**

Organised by C-DAC, SAMEER and C-MET

A two-day programme of plenary addresses, leadership conversations, parallel technical sessions and tutorials.

## Day 1 Programme

Thursday, 26 November 2026 | All times in Indian Standard Time

| Time IST | Programme |
|---|---|
| 09:00 - 10:00 | **Registration and Delegate Welcome**<br>Proposed slot for badge collection and arrival formalities. |
| 10:00 - 11:30 | **Inaugural Session and Exhibition Opening**<br>Welcome remarks, inaugural addresses and opening of the technology exhibition. |
| 11:30 - 11:45 | **Tea and Networking** |
| 11:45 - 12:15 | **Keynote Address**<br>Invited speaker and address title to be confirmed. |
| 12:15 - 12:30 | **Session Transition**<br>Delegate movement and stage preparation. |
| 12:30 - 13:15 | **Fireside Chat \| Sovereign Technologies for Viksit Bharat**<br>Proposed: Secretary, MeitY; industry CEOs; DGs of C-DAC, SAMEER and C-MET. |
| 13:15 - 14:00 | **Lunch and Networking** |
| 14:00 - 16:00 | **Parallel Technical Sessions I**<br>Sessions A1, B1, C1 and D1. See the track programme on page 2. |
| 16:00 - 16:15 | **Tea and Networking** |
| 16:15 - 18:00 | **Parallel Technical Sessions II**<br>Sessions A2, B2, C2 and D2. See the track programme on page 2. |
| 18:00 - 18:30 | **Transition to Evening Programme** |
| 18:30 - 21:00 | **Cultural Programme and Dinner** |

# Day 1 Parallel Sessions

Thursday, 26 November 2026

Tracks A and B are led by C-DAC, Track C by SAMEER, and Track D by C-MET.

### Technical Sessions I

14:00 - 16:00 | 120 minutes | Four simultaneous sessions

| Session | Lead | Theme and indicative focus |
|---|---|---|
| **A1** | C-DAC | **HPC, Exascale and Sovereign Computing**<br>National computing platforms, scalable systems, and scientific and industrial applications. |
| **B1** | C-DAC | **Sovereign Software Development**<br>Cloud platforms, operating systems and software stacks for secure digital infrastructure. |
| **C1** | SAMEER | In progress |
| **D1** | C-MET | **Quantum Materials, Energy Technologies and Advanced Manufacturing**<br>Quantum materials and devices, energy storage and generation, additive manufacturing and packaging technologies. |

### Technical Sessions II

16:15 - 18:00 | 105 minutes | Four simultaneous sessions

| Session | Lead | Theme and indicative focus |
|---|---|---|
| **A2** | C-DAC | **Processors, Semiconductors and Indigenous Accelerators**<br>Processor architectures, design ecosystems, AI accelerators and technology adoption. |
| **B2** | C-DAC | **Artificial Intelligence and Next Generation Cybersecurity**<br>AI applications, trustworthy AI and cyber defence for digital infrastructure. |
| **C2** | SAMEER | In progress |
| **D2** | C-MET | **Semiconductor Materials, Critical Minerals and Circular Economy**<br>Ultra-high-purity materials, critical minerals for electronics, and e-waste recycling and resource recovery. |

# Day 2 Programme

Friday, 27 November 2026 | All times in Indian Standard Time

Plenary addresses are followed by four parallel technical sessions and four parallel tutorials. Delegates reconvene for the collaboration dialogue and closing ceremony.

| Time IST | Programme |
|---|---|
| 09:30 - 10:00 | **Delegate Check-in**<br>Proposed arrival and orientation slot. |
| 10:00 - 10:20 | **Plenary Address I**<br>Invited speaker 1: Speaker and Address title to be confirmed. |
| 10:20 - 10:40 | **Plenary Address II**<br>Invited speaker 2 : Speaker and Address title to be confirmed |
| 10:40 - 11:15 | **Plenary Address III**<br>Invited speaker 3 : Speaker and Address title to be confirmed |
| 11:15 - 11:30 | **Tea and Networking** |
| 11:30 - 13:30 | **Parallel Technical Sessions III**<br>Sessions A3, B3, C3 and D3. See the track programme on page 4. |
| 13:30 - 14:15 | **Lunch and Networking** |
| 14:15 - 16:00 | **Parallel Tutorials and Demonstrations**<br>Tutorials T1-T4 across Tracks A-D. See page 4 for themes and leads. |
| 16:00 - 16:15 | **Tea and Networking** |
| 16:15 - 16:35 | **Fireside Chat \| National and International Partnerships for Viksit Bharat**<br>Collaboration opportunities with C-DAC, SAMEER and C-MET. |
| 16:35 - 17:00 | **Valedictory and Closing Ceremony**<br>Programme highlights, closing remarks and vote of thanks. |

# Day 2 Parallel Sessions and Tutorials

Friday, 27 November 2026

### Technical Sessions III

11:30 - 13:30 | 120 minutes | Four simultaneous sessions

| Session | Lead | Theme and indicative focus |
|---|---|---|
| **A3** | C-DAC | **Robotics, Power Electronics and the Internet of Things**<br>Intelligent systems, industrial control, power conversion and connected devices. |
| **B3** | C-DAC | **Quantum Technologies**<br>Quantum computing, communication, sensing and pathways to practical adoption. |
| **C3** | SAMEER | In progress |
| **D3** | C-MET | **Materials for Healthcare, Energy and Strategic Applications**<br>Medical electronics and healthcare technologies, energy materials, and specialised materials for strategic sectors. |

### Parallel Tutorials

14:15 - 16:00 | 105 minutes | Four simultaneous tutorials

| Track | Lead | Tutorial and indicative scope |
|---|---|---|
| **A / T1** | C-DAC | **VEGA Processor & Ecosystem: From RISC-V Architecture to Real-World IoT Applications**<br>Architecture overview, toolchain demonstration and a guided application walkthrough. |
| **B / T2** | C-DAC | **Silicon Shadows: The Art, Architecture, and Intelligence of Digital Twins**<br>Model setup, data integration and demonstration of a representative use case. |
| **C / T3** | SAMEER | In progress |
| **D / T4** | C-MET | **Exploring Research and Emerging Technologies at C-MET**<br>Overview of C-MET's research areas, emerging materials and device technologies, and potential applications. |